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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/006,269

12/04/2001

Shui-Hung Chen

67,200-537

2058

7590

02/25/2003

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EXAMINER

FARAHANI, DANA

ART UNIT


PAPER NUMBER

2814

DATE MAILED: 02/25/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Continuation of 5. does NOT place the application in condition for allowance because: Yu discloses the SCR will be triggered by an ESD event to pad 1 (see figure 4). reach-through effect defined by applicants as expansion of the collector joint depletion across the base (in page 10). By applying a large enough ESD through pad 1, the depletion region of the collector of transistor 41 (regions 31) reaches across the base .



LONG PHAM
PRIMARY EXAMINER



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